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** CONTINUING DATA *****

HN (N/A)

** FOREIGN APPLICATIONS *****

JAPAN P2003-126597 05/01/2003;

HN (YES)

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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TITLE

Semiconductor memory apparatus and self-repair method

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